CLAIM AMENDMENTS

1. (Currently Amended) A method comprising:

forming a lower electrode;

covering the lower electrode with a protective layer <u>such that said protective layer</u> is formed directly over said lower electrode; and

forming a phase change material over said lower electrode.

2. (Original) The method of claim 1 further comprising:

defining a singulated opening;

forming a cup-shaped phase change material in said opening; and

forming a thermally insulating material in the cup-shaped phase change material.

- 3. (Original) The method of claim 2 including defining said phase change material using a planarization process.
- 4. (Original) The method of claim 3 including defining said phase change material using a chemical mechanical planarization technique.
- 5. (Original) The method of claim 2 including defining a sidewall spacer in said singulated opening.
 - 6. (Original) The method of claim 5 including defining an electrode in said opening.
- 7. (Original) The method of claim 6 including using said sidewall spacer to define the cup-shape of said phase change material.
- 8. (Original) The method of claim 6 including forming a base layer over a substrate and forming said lower electrode over said base layer.
- 9. (Original) The method of claim 1 including sequentially forming said lower electrode and then said protective layer.

10. (Original) The method of claim 9 including etching said lower electrode and said protective film using the same mask.

Claims 11-30 (Cancelled)

- 31. (New) The method of claim 1 including forming the lower electrode and covering the lower electrode with a protective layer in the same chamber.
- 32. (New) The method of claim 31 including depositing the lower electrode and the protective layer in the same deposition chamber.
- 33. (New) The method of claim 32 including depositing the electrode and protective layer in the same deposition chamber without venting back to atmosphere.
- 34. (New) The method of claim 1 including forming the protective layer of an insulator.
- 35. (New) The method of claim 34 including forming the protective layer of a material in the form of silicon nitride.
- 36. (New) The method of claim 35 including forming the silicon nitride in the form of Si_3N_4 .
 - 37. (New) A method comprising:
 forming a protective layer over a lower electrode of a phase change memory.
- 38. (New) The method of claim 37 including forming the lower electrode and covering the lower electrode with a protective layer in the same chamber.
- 39. (New) The method of claim 38 including depositing the lower electrode and the protective layer in the same deposition chamber.

- 40. (New) The method of claim 39 including depositing the electrode and protective layer in the same deposition chamber without venting back to atmosphere.
- 41. (New) The method of claim 37 including forming the protective layer of an insulator.
- 42. (New) The method of claim 41 including forming the protective layer of a material in the form of silicon nitride.
- 43. (New) The method of claim 42 including forming the silicon nitride in the form of Si₃N₄.
- 44. (New) A method comprising:
 forming an insulating protective layer over a conductive lower electrode of a phase change memory.
- 45. (New) The method of claim 44 including forming the lower electrode and covering the lower electrode with a protective layer in the same chamber.
- 46. (New) The method of claim 45 including depositing the lower electrode and the protective layer in the same deposition chamber.
- 47. (New) The method of claim 46 including depositing the electrode and protective layer in the same deposition chamber without venting back to atmosphere.
- 48. (New) The method of claim 44 including forming the protective layer of an insulator.
- 49. (New) The method of claim 48 including forming the protective layer of a material in the form of silicon nitride.

50. (New) The method of claim 49 including forming the silicon nitride in the form of Si_3N_4 .

Respectfully submitted,

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